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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/680,726	10/05/2000	Richard P. Schneider	10004229-1 2517			
75	590 05/21/2003					
Agilent Technologies Legal Department 51U PD Intellectual Property Administration			EXAMINER			
			DIAZ, JOSE R			
PO Box 58043 Santa Clara, C.	A 95052-8043		ART UNIT	PAPER NUMBER		
			2815	2815		
			DATE MAILED: 05/21/2003			

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No		Applicant(s)				
. Office Action Summary		09/680,726	!	SCHNEIDER ET AL.				
<b>'</b>	Office Action Summary	Examiner	1	Art Unit				
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Period to	• •				ess			
THE   - Exte after   - If the   - If NC   - Failu   - Any	ORTENED STATUTORY PERIOD FOR REP MAILING DATE OF THIS COMMUNICATION nsions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication. period for reply specified above is less than thirty (30) days, a re period for reply is specified above, the maximum statutory period re to reply within the set or extended period for reply will, by statue ply received by the Office later than three months after the mailing day patent term adjustment. See 37 CFR 1.704(b).	136(a). In no event, how only within the statutory middle will apply and will expire te. cause the application is	ever, may a reply be timely nimum of thirty (30) days w SIX (6) MONTHS from the o become ARANDONED	r filed  iill be considered timely. mailing date of this comm	nunication.			
1)	Responsive to communication(s) filed on 03	March 2003						
2a)⊠		his action is non-f	inal					
3)	Since this application is in condition for allow			courtion as to the	aarita ia			
,—	closed in accordance with the practice unde on of Claims	Ex parte Quayle,	1935 C.D. 11, 453	3 O.G. 213.	ients is			
4)🖂	Claim(s) 3-32 is/are pending in the application	n.						
	4a) Of the above claim(s) is/are withdra	awn from consider	ation.					
5)	Claim(s) is/are allowed.							
6)⊠	Claim(s) <u>3-32</u> is/are rejected.							
7)	Claim(s) is/are objected to.							
8)[	Claim(s) are subject to restriction and/	or election require	ment.					
Applicati	on Papers	•						
9) 🔲 -	The specification is objected to by the Examin	er.						
10) The drawing(s) filed on is/are: a) □ accepted or b) □ objected to by the Examiner.								
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.								
If approved, corrected drawings are required in reply to this Office action.								
12) 🔲 🗆	he oath or declaration is objected to by the E	xaminer.						
Priority u	nder 35 U.S.C. §§ 119 and 120							
13)	Acknowledgment is made of a claim for foreig	n priority under 35	5 U.S.C. § 119(a)-(	d) or (f).				
a)[	a) All b) Some * c) None of:							
	1. Certified copies of the priority documen	ts have been rece	ived.					
	2. Certified copies of the priority documents have been received in Application No							
	3. Copies of the certified copies of the pric application from the International Bu ee the attached detailed Office action for a list	ireau (PCT Rule 1	7.2(a)).	n this National Sta	ge			
	cknowledgment is made of a claim for domest			to a provisional ap	plication).			
_a)	☐ The translation of the foreign language pr cknowledgment is made of a claim for domes	ovisional applicati	on has been receiv	ed.	,			
Attachment	(s)							
2) Notice	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449) Paper No(s) _	4)		TO-413) Paper No(s) ent Application (PTO-15				
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#### **DETAILED ACTION**

## Claim Rejections - 35 USC § 102

➤ The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

➤ Claims 3-32 are rejected under 35 U.S.C. 102(b) as being anticipated by Jewell (US Pat. No. 5,859,864).

Regarding claims 3-4, 12, 15-17, 25 and 28-32, Jewell teaches a VCSEL device (see Fig. 4a) comprising: a device structure, having a height z and an aperture (114), including an active layer (110), and upper and lower DBR (102, 116); contacts (122, 124); and a light emission property that varies within the aperture and the light output is in spatially fixed modes (see col. 5, lines 5-17). Furthermore, Jewell teaches a non-planar VCSEL structure (see Figs. 2a-2d and 3a-3b), wherein a phase mismatch is provided. Such VCSEL properties disclosed by Jewel are the same properties that Applicant intents to claim, hence it is inherent that by providing a non planar structure and a phase mismatch, a light emission property such as refractive index and Fabry-Perot wavelengths varies within the aperture and the light output is in spatially fixed modes.

Regarding claim 5, Jewell teaches a non-planar layer (consider the mesa 26) and a substrate (52) adjacent to the DBR (102) (see Fig. 4a).

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Regarding claims 6-7, 9, 11, 14, 19-20, 22, 24, and 27, Jewell teaches that the non-planar layer is a texturing layer (consider the fact that the substrate, which comprises more than one layer, is patterned to create the mesa structure 26). See col. 7, lines 43-46 and col. 11, lines 60-66 and col. 12, lines 30-37. Furthermore, with regards to the process steps of patterning or polishing described in the claims. Applicant should noted that such limitations contain method of making characteristics given no patentable weight in determining patentability of the final device structure. Note that a "product by process" claim is directed to the product per se, no matter how actually made, In re Brown, 173 USPQ 685; In re Luck, 177 USPQ 523; In re Fessmann, 180 USPQ 324; In re Avery, 186 USPQ 161; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); In re Marosi et al, 218 USPQ 289; and particularly In re Thorpe, 227 USPQ 964, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not. Note that applicant has the burden of proof in such cases, as the above case law makes clear.

Regarding claims 8, 13, 21, and 26, Jewell teaches that the non-planar layer (26) is a layer within at least one of the upper or lower DBR (102) (see Fig. 4a).

Regarding claims 10, 18 and 23, Jewell teaches that the non-planar layer (26) is a first surface of the substrate (52) adjacent to the lower DBR (102) (see Fig. 4a).

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➤ Claims 3-32 are rejected under 35 U.S.C. 102(b) as being anticipated by Lebby et al. (US Pat. No. 5,848,086).

Regarding claims 3-4, 12, 15-17, 25, 28 and 30-32, Lebby et al. teach a non-planar VCSEL device (see Figs. 1-2) comprising: a device structure, having a height z and an aperture (not shown), including an active layer (117), and upper and lower DBR (109, 127); contacts (112, 132); and a light emission property that varies within the aperture and the light output is in spatially fixed modes (see col. 8, lines 30-36). Furthermore, the VCSEL property disclosed by Lebby et al. is the same property that Applicant intents to claim, hence it is inherent that by providing a non planar structure, a light emission property such as refractive index and Fabry-Perot wavelengths varies within the aperture and the light output is in spatially fixed modes.

Regarding claim 5, Lebby et al. teach a non-planar layer (155, 256) and a substrate (101) adjacent to the DBR (109) (see Figs. 1-2).

Regarding claims 6-7, 9, 11, 14, 19-20, 22, 24, and 27, Lebby et al. teach that the non-planar layer is a texturing layer (consider the fact that the layers 155 and/or 256 are patterned). See Figs. 1-2 and col. 3, lines 3-41. Furthermore, with regards to the process steps of patterning or polishing described in the claims, Applicant should noted that such limitations contain method of making characteristics given no patentable weight in determining patentability of the final device structure. Note that a "product by process" claim is directed to the product per se, no matter how actually made, In re Brown, 173 USPQ 685; In re Luck, 177 USPQ 523; In re Fessmann, 180 USPQ 324; In re Avery, 186 USPQ 161; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal

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with this issue); In re Marosi et al, 218 USPQ 289; and particularly In re Thorpe, 227 USPQ 964, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not. Note that applicant has the burden of proof in such cases, as the above case law makes

Regarding claims 8, 13, 21, and 26, Lebby et al. teach that the non-planar layer (155, 256) is a layer within at least one of the upper or lower DBR (109) (see Figs. 1-2).

Regarding claims 10, 18 and 23, Lebby et al. teach that the non-planar layer (155, 256) is a first surface of the substrate (101) adjacent to the lower DBR (109) (see Figs. 1-2).

Regarding claim 29, Lebby et al. further teach removing the substrate after the step of forming electrical contacts (see col. 6, lines 37-54).

#### Response to Arguments

> Applicant's arguments filed on March 3, 2003 have been fully considered but they are not persuasive. With regards to the arguments about the "textured surface", Applicant teaches in the specification that the step of texturizing a surface" is done by applying, for example, a lithographic or an etching process (see page 7, lines 15-19). The reference Jewell discloses such texturizing step in col. 11, lines 60-64, wherein Jewell teaches texturing the surface by performing an etching process. And, the

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reference Lebby et al. discloses such texturizing step in col. 3, lines 5-7, wherein Lebby et al. teach the step of texturing the surface by etching or lithographic. Thus, the rejection is considered to be proper since both Jewell and Lebby et al. teach the required textured surface.

#### Conclusion

> THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

### Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to José R Díaz whose telephone number is (703) 308-6078. The examiner can normally be reached on 9:00-5:00 Monday, Tuesday, Thursday and Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on (703) 308-1690. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 746-3891 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

JRD May 18, 2003

> EDDIE LEE SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800